IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.:

09/452,591

Confirmation No. 1638

Applicant:

Fred D. Bailey et al.

Filed:

12/02/1999

TC/A.U.:

2815

Examiner:

Jesse A. Fenty

Docket No.: Customer No.: TI-27935

23494

Title:

AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED

WITHIN A MULTILEVELDIELECTRIC BETWEEN AN UPPER AND LOWER

METAL INTERCONNECT LAYER

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9318 on the date shower below

Jacqueline J. Garner, Reg. No. 36,144

November 3, 2003

Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

Two Months (\$420)

Three Months (\$950).

. . Four Months (\$1,480)

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 Fax. (972) 917-4418

Respectfully_Submitted.

Jacqueline J. Garner Reg. No. 36,144